Transistors Panasonic

# 2SD1820

## Silicon NPN epitaxial planar type

For general amplification Complementary to 2SB1219

### ■ Features

- ullet Low collector-emitter saturation voltage  $V_{\text{CE(sat)}}$
- S-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape packing.

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	$V_{CBO}$	30	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	25	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	5	V	
Collector current	$I_{C}$	500	mA	
Peak collector current	I <sub>CP</sub>	1	A	
Collector power dissipation	P <sub>C</sub>	150	mW	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

### ■ Package

- Code SMini3-G1
- Pin Name
  - 1. Base
  - 2. Emitter
  - 3. Collector
- Marking Symbol: W

■ Electrical Characteristics T<sub>a</sub> = 25°C±3°C

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Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 10 \mu\text{A}, I_E = 0$	30			V
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 2 \text{ mA}, I_B = 0$	25			V
Emitter-base voltage (Collector open)	$V_{\rm EBO}$	$I_{\rm E} = 10  \mu \text{A}, I_{\rm C} = 0$	5			V
Collector-base cutoff current (Emitter open)	$I_{CBO}$	$V_{CB} = 20 \text{ V}, I_{E} = 0$			0.1	μΑ
Forward current transfer ratio *1	h <sub>FE1</sub> *2	$V_{CE} = 10 \text{ V, } I_{C} = 150 \text{ mA}$	85		340	
	h <sub>FE2</sub>	$V_{\rm CE} = 10 \text{ V}, I_{\rm C} = 500 \text{ mA}$	40			
Collector-emitter saturation voltage *1	V <sub>CE(sat)</sub>	$I_C = 300 \text{ mA}, I_B = 30 \text{ mA}$		0.35	0.60	V
Transition frequency	$\mathcal{O}_{\mathrm{f}_{\mathrm{T}}}$	$V_{CB} = 10 \text{ V}, I_{E} = -50 \text{ mA}, f = 200 \text{ MHz}$		200		MHz
Collector output capacitance	$C_{ob}$	$V_{CR} = 10 \text{ V}, I_F = 0, f = 1 \text{ MHz}$		6	15	рF
(Common base, input open circuited)	ob	v <sub>CB</sub> - 10 v, 1 <sub>E</sub> - 0, 1 - 1 will z			13	hı.

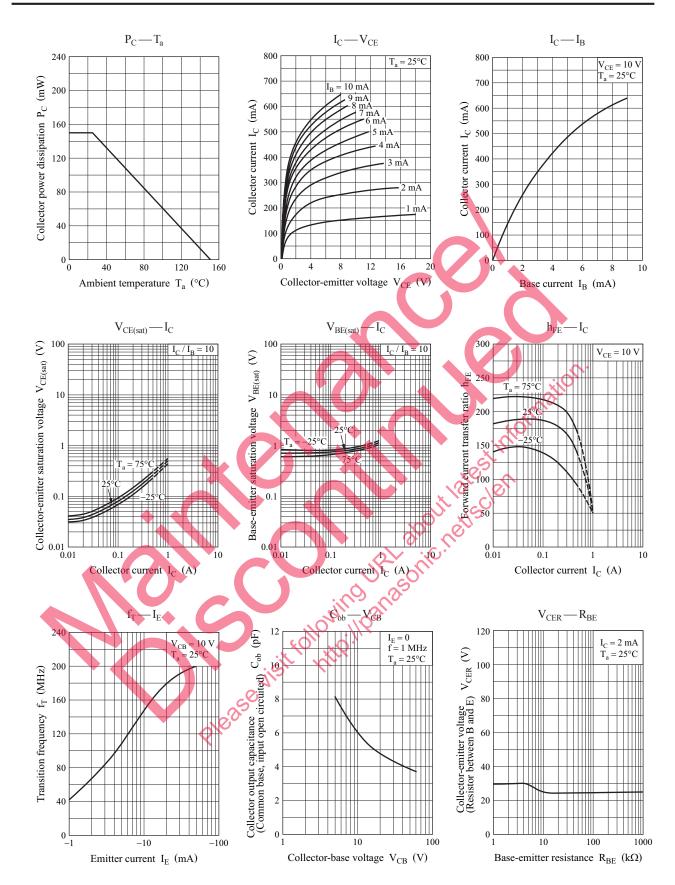
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- 2. \*1: Pulse measurement
  - \*2: Rank classification

Rank	Q	R	S	No-rank
$h_{\mathrm{FE1}}$	85 to 170	120 to 240	170 to 340	85 to 340
Marking symbol	WQ	WR	WS	_

Product of no-rank is not classified and have no marking symbol for rank.

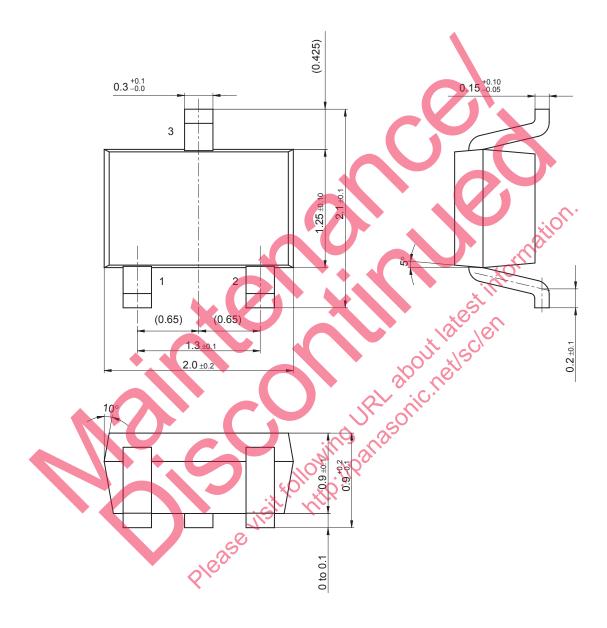
2SD1820 Panasonic



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Panasonic 2SD1820

SMini3-G1 Unit: mm



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